#### IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

CHANG, Shu-Ming et al

Appl. No.:

NEW

Group:

Filed:

January 29, 2004

Examiner:

For:

WAFER LEVEL CHIP SCALE PACKAGING

STRUCTURE AND METHOD OF FABRICATING THE

SAME

INFORMATION DISCLOSURE STATEMENT
(SUBMISSION CONCURRENT WITH THE
FILING OF A NEW PATENT APPLICATION)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

January 29, 2004

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

## I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION

The patents, publications, or other information submitted for consideration by the Office are listed on PTO-1449, attached hereto.

### II. COPIES

- This application was filed before June 30, 2003.

  Accordingly, submitted herewith is a legible copy of (i) each U.S. and foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed.
- b. A This application was filed on or after June 30, 2003. Accordingly, copies of cited US patents and patent application publications therefore are not included. Copies of foreign patent documents and non-patent literature are included.

c. This application is a National Phase of a PCT application. Some or all of the documents listed on the PTO-1449 are not enclosed because they were cited in the International Search Report and copies should be forwarded from the International Search Authority. If copies are needed, please contact the undersigned.

# III. CONCISE EXPLANATION OF THE RELEVANCE (check at least one box)

a. DOCUMENTS IN THE ENGLISH LANGUAGE

The patents, publications, or other information listed on the attached PTO 1449 are in the English language and therefore, do not require a statement of relevancy.

b. DOCUMENTS NOT IN THE ENGLISH LANGUAGE

A concise explanation of the relevance of all patents, publications, or other information listed that is not in the English language is as follows:

c. ENGLISH LANGUAGE SEARCH REPORT

An English language version of the search report or action that indicates the degree of relevance found by the foreign office is attached, thereby satisfying the requirement for a concise explanation. See MPEP 609 (III) (A) (3).

The following additional information is provided for the Examiner's consideration.

#### FEES

This Information Disclosure Statement is being filed concurrently with the filing of a new patent application; therefore, no fee is required.

If the Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule and charge the appropriate fee to Deposit Account No. 02-2448.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under § particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

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Falls Church, VA 22040 \ 0747

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Attachment(s):	$\boxtimes$	Form PTO-1449(s)
	$\boxtimes$	Documents
		Foreign Search Report
		Fee
		Other:

KM/tmr

3313-1099P

(Rev. 09/30/03)

Form PTO-1449					ATTY. DOCKET NO. 3313-1099P		APPLICATION NO. NEW					
INFORMATION DISCLOSURE CITATION IN AN APPLICATION				APPLICANT CHANG, Shu-Ming et al								
-	(Use several sheets if necessary)				FILING DATE January 29, 20	004	GROUP					
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EXAMINER INITIAL	DOCU	MENT NUMBER	Kind	DATE	NAME	CLASS	SUB CLASS	FILIN IP APPE	G DATE COPRIATE			
	US 6,023,103		A	2000-02-08	Chang et al							
	US 2002/0127768		A1	2002-09-12	Badir et al	-						
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journal, se	rial, symposi	ium, catalog, etc.) date	, page(s), vol	ume-issue number(s), pub	title of the article (when appro- lisher, city and/or country where	e published.						
	Hamano Sympos	o et al, "Super sium on Advance	r CSP™: ' ed Packag	WLCSP Solution ging Materials	for Memory and Sys , 1999, pages 221-	tem LSI 225	" Inter	nation	al			
	Kazama et al, "Development of Low-cost and Highly Reliable Wafer Process Package" Electronic Components and Technology Conference, 2001, 7 pages											
EXAMINER					DATE CONSIDERED							
EXAMINER: I	nitial if cit	ation considered, wheth	er or not cita	tion is in conformance w	ith M.P.E.P. 609; Draw line thro	ugh citation	if not in co	nformance a	and not			
considered.	Include cop	y of this form with nex	t communication	n to applicant.								